

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	9	@ad<="20030915" and 'cell' same 'logic' same 'cell gate oxide' and 'polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/01 15:42
S2	30	@ad<="20030915" and 'SRAM' same 'dual' with 'polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/01 15:53
S3	6963	@ad<="20030915" and (257/382). ccls. or (257/532).ccls. or (257/755).ccls. or (257/763).ccls. or (257/E21.507).ccls. or (257/E21.59).ccls. or (257/E21.593).ccls. or (257/E21.66).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/15 14:41
S4	3125	@ad<="20030915" and (438/238). ccls. or (438/586).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/14 09:55
S5	1	"6080647".PN.	USPAT; USOCR	OR	ON	2004/12/06 16:19
S6	1	"5866451".PN.	USPAT; USOCR	OR	ON	2004/12/06 16:19
S7	5938	@ad<="20030915" and (438/621). ccls. or (438/655).ccls. or (438/656).ccls. or (438/657).ccls. or (438/253).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 16:29
S8	1	"6294420".PN.	USPAT; USOCR	OR	ON	2004/12/06 16:22
S9	1	"6177340".PN.	USPAT; USOCR	OR	ON	2004/12/06 16:22
S10	2030	@ad<="20030915" and (438/585). ccls. or (438/152).ccls. or (438/594).ccls. or (438/734).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 16:29
S12	159	@ad<="20030915" and 'SRAM' same 'polysilicon layers' and 'cell' and 'logic'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 17:21
S13	122	@ad<="20030915" and 'SRAM' same 'polysilicon layers' and 'cell' and 'peripheral'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/15 14:42

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S14	2188	@ad<="20030915" and (438/253). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 17:03
S15	174	@ad<="20030915" and 'SRAM' same 'first polysilicon' and 'second polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 07:07
S16	669	@ad<="20030915" and 'SRAM' and 'first polysilicon' and 'second polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 17:22
S17	543	@ad<="20030915" and 'SRAM' and 'first polysilicon' and 'second polysilicon' and 'cell' and 'device'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/06 17:23
S18	304	@ad<="20030915" and 'SRAM' and 'first polysilicon' and 'second polysilicon' and 'cell' same 'device' same 'region'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 08:28
S19	653	@ad<="20030915" and 'SRAM' same 'memory' and 'peripheral' and 'polysilicon layers'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 08:29
S20	301	@ad<="20030915" and 'SRAM' and 'first polysilicon' and 'second polysilicon' and 'memory' same 'device' same 'region'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 08:29
S21	9	@ad<="20030915" and 'SRAM' same 'memory' same 'peripheral' same 'polysilicon layers'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 08:29
S22	811	@ad<="20030915" and (257/903). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 10:23
S23	324	@ad<="20030915" and (257/380-381).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 12:40
S24	138	@ad<="20030915" and (257/385). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 12:41

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S25	260	@ad<="20030915" and (257/756). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 12:41
S26	501	@ad<="20030915" and (438/647). ccls. or (438/657).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/14 09:52
S27	196	@ad<="20030915" and (438/630). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 12:45
S28	7129	@ad<="20030915" and (257/382). ccls. or (257/532).ccls. or (257/755).ccls. or (257/763).ccls. or (257/E21.507).ccls. or (257/E21. 59).ccls. or (257/E21.593).ccls. or (257/E21.66).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/15 14:41
S29	3176	@ad<="20030915" and (438/238). ccls. or (438/586).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 10:12
S30	123	@ad<="20030915" and 'SRAM' same 'polysilicon layers' and 'cell' and 'peripheral'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/15 14:43
S31	123	@ad<="20030915" and 'SRAM' same 'polysilicon layers' and 'cell' and 'peripheral'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:39
S32	10	@ad<="20030915" and 'cell' same 'logic' same 'cell gate oxide' and 'polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/01 15:42
S35	0	@ad<="20030915" and 'cell' with 'gate oxide' and 'logic' with 'gate oxide' and 'doped polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/01 15:56
S36	726	@ad<="20030915" and 'cell' with 'oxide' and 'logic' with 'oxide' and 'polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/01 15:57

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S37	215	@ad<="20030915" and 'cell' with 'oxide' and 'logic' with 'oxide' and 'doped polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/01 15:57
S38	179	'SRAM' same 'first polysilicon' and 'second polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 08:05
S39	720	'SRAM' and 'first polysilicon' and 'second polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 07:08
S40	1163	'memory' and 'peripheral' and 'first polysilicon' and 'second polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 08:06
S41	1129	'memory' and 'logic' and 'first polysilicon' and 'second polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 08:23
S42	19	'memory' with 'dual gate' and 'logic' and 'first polysilicon' and 'second polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 08:21
S43	1	"5155056".PN.	USPAT; USOCR	OR	ON	2005/09/02 08:17
S44	1	"5395784".PN.	USPAT; USOCR	OR	ON	2005/09/02 08:18
S45	684	'memory' same 'logic' and 'first polysilicon' same 'second polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 08:23
S46	78	@ad<="20030915" and 'memory' and 'logic' and 'dual' with 'polysilicon gate'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:31
S47	1	"5656839".PN.	USPAT; USOCR	OR	ON	2005/09/02 09:33
S48	1	"5736421".PN.	USPAT; USOCR	OR	ON	2005/09/02 09:33
S49	1	"20030062566".PN.	US-PGPUB	OR	ON	2005/09/02 09:34

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S50	162	@ad<="20030915" and 'SRAM' same 'polysilicon layers' and 'cell' and 'logic'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:40
S51	19	@ad<="20030915" and 'SRAM' same 'dual' same 'polysilicon' and 'logic'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:41
S52	7	@ad<="20030915" and 'SRAM' same 'dual' same 'polysilicon' and 'peripheral'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:41
S53	5	@ad<="20030915" and 'SRAM' with 'dual' same 'polysilicon' and 'peripheral'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:51
S54	1	@ad<="20030915" and 'SRAM' and 'logic gate' and 'cell gate' same 'doped polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:52
S55	122	@ad<="20030915" and 'SRAM' and 'logic gate' and 'cell gate'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:53
S56	0	@ad<="20030915" and 'SRAM' and 'logic gate' and 'cell gate' with 'dual' with 'polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:53
S57	437	@ad<="20030915" and 'memory' same 'gate' same 'dual' same 'polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:54
S58	194	@ad<="20030915" and 'memory' with 'gate' same 'dual' with 'polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:55
S59	1	"6432768".PN.	USPAT; USOCR	OR	ON	2005/09/02 10:03
S60	1	"6391704".PN.	USPAT; USOCR	OR	ON	2005/09/02 10:05
S61	1	"6218715".PN.	USPAT; USOCR	OR	ON	2005/09/02 10:06
S62	1	"6218715".PN.	USPAT; USOCR	OR	ON	2005/09/02 10:06

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S63	1	"6168984".PN.	USPAT; USOCR	OR	ON	2005/09/02 10:07
S64	1	"6096595".PN.	USPAT; USOCR	OR	ON	2005/09/02 10:07
S65	1	"6015730".PN.	USPAT; USOCR	OR	ON	2005/09/02 10:11
S66	1	"5998252".PN.	USPAT; USOCR	OR	ON	2005/09/02 10:11
S67	1	"5863820".PN.	USPAT; USOCR	OR	ON	2005/09/02 10:12
S69	1921	@ad<="20030915" and (438/257). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 10:13
S70	634	@ad<="20030915" and (438/241). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 10:20
S71	232	@ad<="20030915" and (438/157). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 10:17
S72	282	@ad<="20030915" and (438/152). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 10:17
S75	532	@ad<="20030915" and ('memory' or 'DRAM' or 'SRAM') and ('peripheral' or logic) and 'dual' with 'polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 08:19
S76	35	@ad<="20030915" and ('memory' or 'DRAM' or 'SRAM') and ('peripheral' or logic) and 'dual' with 'doped polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 08:36
S77	1076	@ad<="20030915" and 'SRAM' and ('peripheral' or logic) and 'doped polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 08:36
S78	841	@ad<="20030915" and 'SRAM' and 'logic' and 'doped polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 08:42

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S79	2	@ad<="20030915" and 'SRAM' and 'logic' and 'dual' with 'doped' with 'polysilicon gate'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 08:43
S80	79	@ad<="20030915" and 'memory' and 'logic' and 'dual' with 'polysilicon gate'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 08:58
S81	1027	@ad<="20030915" and 'memory' and 'logic' and 'first polysilicon' and 'second polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 08:59
S82	230	@ad<="20030915" and 'SRAM' and 'logic' and 'first polysilicon' and 'second polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 08:59
S83	1	"6506647".PN.	USPAT; USOCR	OR	ON	2005/12/28 09:07
S84	640	@ad<="20030915" and (438/241).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 10:54
S85	1035	@ad<="20030915" and (438/238).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 09:05
S86	810	@ad<="20030915" and (257/e21.661).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 09:46
S87	828	@ad<="20030915" and (257/903).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 09:08
S88	642	@ad<="20030915" and (257/e27.098).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 09:08
S89	332	@ad<="20030915" and (365/185.08).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 09:45

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S90	298	@ad<="20030915" and (438/210). cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/28 10:54
S91	61	('cell' and 'peripheral' and ('first' with 'polysilicon') and ('second' with 'polysilicon') and 'gate oxide').clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/03 10:46
S92	225	@ad<="20030915" and 'memory' and 'peripheral' and 'gate oxide' same 'first polysilicon' same 'second polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/03 10:11
S93	104	@ad<="20030915" and 'memory' and 'peripheral' and 'gate oxide' with 'first polysilicon' with 'second polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/03 09:11
S94	125	@ad<="20030915" and 'SRAM' and 'gate oxide' same 'first polysilicon' same 'second polysilicon'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/03 10:12
S95	1	"5550085".PN.	USPAT; USOCR	OR	ON	2006/01/03 10:26
S97	746	@ad<="20030915" and 'cell' and 'peripheral' and 'first polysilicon' and 'second polysilicon' and 'gate oxide'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 08:58
S99	27	@ad<="20030915" and "SRAM" same (peripheral or logic) same (cell or memory) same dope\$3 with "polysilicon"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 09:14
S10 0	30	@ad<="20030915" and "SRAM" same (peripheral or logic) same (cell or memory or array) same dope\$3 with "polysilicon"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/23 15:50
S10 1	1	"5264716".PN.	USPAT; USOCR	OR	ON	2006/03/24 08:38
S10 2	1	"5173439".PN.	USPAT; USOCR	OR	ON	2006/03/24 08:44
S10 3	1	"5539229".PN.	USPAT; USOCR	OR	ON	2006/03/24 08:53
S10 4	1	"5448090".PN.	USPAT; USOCR	OR	ON	2006/03/24 08:54
S10 5	1	"5369049".PN.	USPAT; USOCR	OR	ON	2006/03/24 08:55

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S10 6	1	"5298450".PN.	USPAT; USOCR	OR	ON	2006/03/24 08:55
S10 7	1	"5264716".PN.	USPAT; USOCR	OR	ON	2006/03/24 08:56
S10 8	922	@ad<="20030915" and (438/239). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 09:09
S10 9	653	@ad<="20030915" and (257/e27. 098).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 09:46
S11 0	830	@ad<="20030915" and (257/903). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 09:46
S11 1	413	@ad<="20030915" and (257/904). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 09:08
S11 2	827	@ad<="20030915" and (257/e21. 661).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 09:08
S11 3	333	@ad<="20030915" and (365/185. 08).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 09:09
S11 4	1040	@ad<="20030915" and (438/238). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 09:09
S11 5	2316	@ad<="20030915" and (438/253). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 09:09
S11 6	286	@ad<="20030915" and (438/152). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 09:09
S11 7	156	@ad<="20030915" and "SRAM" same (peripheral or logic) same (cell or memory) same "polysilicon"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 10:00

EAST Search History

S11 8	4	@ad<="20030915" and ("SRAM" or "DRAM") same (peripheral or logic) same (cell or memory) same "dual polysilicon"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 10:27
S11 9	2	("5451534").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/24 10:11
S12 0	1	"5602049".PN.	USPAT; USOCR	OR	ON	2006/03/24 10:21
S12 1	13	@ad<="20030915" and (peripheral or logic) same (cell or memory) same "dual polysilicon"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 10:27
S12 2	7	("4792841" "5187566" "5412237" "5451534" "5486717" "5780887").PN. OR ("5903035").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/08/04 09:38
S12 3	2	("5451534").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/04 09:38
S12 4	18	("4876215" "4987092" "5198683" "5266507" "5298782" "5354704").PN. OR ("5451534").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/08/04 09:39
S12 5	336	@ad<="20030915" and (365/185.08).cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 09:45
S12 6	701	@ad<="20030915" and (257/e27.098).cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/14 10:04
S12 7	926	@ad<="20030915" and (257/e21.661).cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 09:46
S12 8	833	@ad<="20030915" and (257/903).cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/14 09:55

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S13 1	326	@ad<="20030915" and "SRAM" and (peripheral or logic) and "first polysilicon" and "second polysilicon"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/13 16:15
S13 2	2	("6489202").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/14 09:52
S13 3	833	@ad<="20030915" and (257/903). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/14 09:55
S13 4	417	@ad<="20030915" and (257/904). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/14 09:55
S13 5	1981	@ad<="20030915" and (438/257). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/14 09:56
S13 6	783	@ad<="20030915" and (438/258). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/14 09:56
S13 7	650	@ad<="20030915" and (438/241). ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/14 09:57
S13 8	718	@ad<="20030915" and (257/e27. 098).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/14 10:04